

Table 1 Details of control made by CTL terminal

CTL Potential	Delay time mode 1 (Fuse 1 is not cut)	Delay time mode 2 (Fuse 1 is cut)
L: normal state	<p>Oscillator oscillates at normal oscillation frequency</p> <p>Overcharge detection delay time</p> $Tc=2^{n-1} \cdot Tclk$ <p>Overdischarge detection delay time</p> $Td=2^{n-1} \cdot Tclk$ <p>Other delay time</p> $Tx=2^{x-1} \cdot Tclk$	<p>Oscillator oscillates at normal oscillation frequency</p> <p>Overcharge detection delay time</p> $Tc=2^{n-1} \cdot Tclk$ <p>Overdischarge detection delay time</p> $Td=2^{n-1} \cdot Tclk$ <p>Other Delay Time</p> $Tx=2^{x-1} \cdot Tclk$
M: test state	<p>Oscillation frequency of oscillator is accelerated by K times</p> <p>Overcharge detection delay time</p> $Tc=Tclk/K$ <p>Overdischarge detection delay time</p> $Td=Tclk/K$ <p>Other delay time</p> $Tx=2^{x-1} \cdot Tclk/K$	<p>Oscillation frequency of oscillator is accelerated by K times</p> <p>Overcharge detection delay time</p> $Tc=2^{n-1} \cdot Tclk/K$ <p>Overdischarge detection delay time</p> $Td=2^{n-1} \cdot Tclk/K$ <p>Other delay time</p> $Tx=2^{x-1} \cdot Tclk/K$
H: charging and discharging prohibition state	Charging and discharging prohibition	Charging and discharging prohibition